

TO ALL TO WINDE THIESE PRESENTS SHAME (COME):

UNITED STATES DEPARTMENT OF COMMERCE

United States Patent and Trademark Office

March 16, 2006

THIS IS TO CERTIFY THAT ANNEXED HERETO IS A TRUE COPY FROM THE RECORDS OF THE UNITED STATES PATENT AND TRADEMARK OFFICE OF THOSE PAPERS OF THE BELOW IDENTIFIED PATENT APPLICATION THAT MET THE REQUIREMENTS TO BE GRANTED A FILING DATE.

APPLICATION NUMBER: 60/516,247

FILING DATE: October 31, 2003

RELATED PCT APPLICATION NUMBER: PCT/US04/36542

THE COUNTRY CODE AND NUMBER OF YOUR PRIORITY APPLICATION, TO BE USED FOR FILING ABROAD UNDER THE PARIS CONVENTION, IS *US60/516,247*

CHARLES

Certified by

Under Secretary of Commerce for Intellectual Property and Director of the United States Patent and Trademark Office

PTO/SB/16 (8-03)

Approved for use through 7/31/2003. OMB 0551-0032

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PROVISIONAL APPLICATION FOR PATENT COVER SHEET

This is a request for filing a PROVISIONAL APPLICATION FOR PATENT under 37 CFR 1.53 (c).

Express Mail Label No.	EV 342472551 US

	INVENTOR(S	5)				
Given Name (first and middle [if any])	Resident Family Name or Surname (City and either State		Residence and either State or F	te or Foreign Country)		
Keyue Taotao	SMEDLEY JIN		Aliso Viejo, Cali Irvine, Califo	ifornia mia		
Additional inventors are being named on theseparately numbered sheets attached hereto						
TITLE OF THE INVENTION (500 characters max) UNIVERSAL ONE CYCLE CONTROL VECTOR CONTROLLERS						
CORRESPONDENCE ADDRESS						
Direct all correspondence to: Customer Number	34313					
OR Type Customer Number here						
Firm or Individual Name	<u> </u>					
Address						
Address						
City						
Country						
ENCL	OSED APPLICATION PART	S (check all that apply)				
Specification Number of Pages 5 CD(s),						
☐ Drawing(s) Number of Sheets ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐						
Application Data Sheet. See 37 CFR 1.76 Appendix A						
\ _ ··						
METHOD OF PAYMENT OF FILING FEES FOR THIS PROVISIONAL APPLICATION FOR PATENT (check one)						
Applicant daims small entity status. See 37 CFR 1.27.						
A check or money order is enclosed	AMOUNT (\$)					
The Commissioner is hereby authorized to charge filing fees or credit any overpayment to Deposit Account Number: 15-0665 \$80.00						
Payment by credit card. Form PTC)-2038 is attached.			\$80.00		
The invention was made by an agency of the United States Government or under a contract with an agency of the United States Government.						
_						
No. Yes, the name of the U.S. Government agency is and the Government contract number is .						
Respectfully submitted	Slaf	Date CREGISTRATION NO	October 31, 2003			
	h S. Roberts	(if appropriate) Docket Number:	703538.4029	<u></u>		
TELEPHONE 949-567-6700						

USE ONLY FOR FILING A PROVISIONAL APPLICATION FOR PATENT

This collection of information is required by 37 CFR 1.51. The information is used by the public to file (and by the PTO to process) a provisional application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 8 hours to complete, including gathering, and submitting the complete provisional application to the PTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for rethering this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Box Provisional Application, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

DOCSOC1:143810.1 703538.4029

UNITED STATES PATENT APPLICATION FOR

UNIVERSAL ONE CYCLE CONTROL VECTOR CONTROLLERS

INVENTORS:

KEYUE SMEDLEY TAOTAO JIN

PREPARED BY:

ORRICK, HERRINGTON & SUTCLIFFE LLP FOUR PARK PLAZA, SUITE 1600 IRVINE, CA 92614-2558

Universal One Cycle Control Vector Controllers

FIELD OF THE INVENTION

[001] The invention relates generally to the field of power converter controllers, and more particularly to systems and methods for universal one cycle control vector controllers.

BACKGROUND INFORMATION

Power converters are indispensable elements in Distributed Generation (DG) power systems. A power converter such as a power factor corrected (PFC) rectifier or an active power filters (APF) can be used to suppress the harmonics generated by loads present in the DG system. A static synchronous compensator (STATCOM) type power converter can be used to control the power flow in the DG power grid, while a grid-connected inverter can form the key bridge between most of the renewable energy sources and the power grid. In recent years, research and applications of PFC rectifiers, Active Power Filters (APF), STATCOMs, and grid-connected inverters using voltage-source converters (VSCs) have attracted more attention due to the increased energy awareness across the globe. Most reported control methods employ DQ conversion and real-time reference current calculation. These methods require a high-speed digital microprocessor and high performance A/D converters and result in a higher cost, higher complexity and lower reliability.

[003] Prior research has demonstrated that one-cycle control is a unified pulse width modulation (PWM) control method viable in the power electronics field. One cycle control can be analogized to an analog computer, capable of controlling all basic power converters with relatively lower costs, lower complexity and higher reliability. But in each application the detailed control circuits based on one cycle control are different, a fact which results in several different control chips for controlling a three-phase PFC rectifier, APF, STATCOM, and grid connected inverter respectively.

BRIEF DESCRIPTION OF THE INVENTION

The present invention is directed to systems and methods that allow for the control of a power converter using a three phase, multilevel voltage source converter and a one cycle control methodology. In one example embodiment, a one cycle control voltage source controller is connected to a transitor-based module to implement a power converter, such as a PFC rectifier, an APF, a STATCOM or a grid connected inverter. One of the purposes of the system and methods is to develop simple, reliable, low cost, and easy-to-use power electronics for industrial and power system applications.

[005] Preferably, one cycle control is used to implement a universal control circuit capable of controlling PFC rectifiers, APFs, STATCOMs, and inverters in both two level and three level configurations. The universal control circuit can preferably control multiple types of power converters on a single integrated circuit chip, creating more practical applications for renewable energy and distributed generation and more powerful control functions. The industrial applications can include PFC rectifiers and inverters for motor drives, commercial electronics products, etc. The power system applications can include APF for power quality control, STATCOM for flow control, inverters for renewable/alternative energy power generation.

[006] In addition, the systems and methods described herein provide control key equations and circuits for a one cycle controller controlled PFC rectifier, APF, STATCOM, and grid connected inverter, as well as numerous modulation embodiments for diversifying the range of applications in which the universal control circuit can be applied.

[007] The present invention is further illustrated by Appendix A, which is incorporated herein by reference as if set out in its entirety.

[008] In the foregoing specification, the invention has been described with reference to specific embodiments thereof. It will, however, be evident that various modifications and changes may be made thereto without departing from the broader spirit and scope of the invention. As an example, each feature of one embodiment can be mixed and matched with other features shown in other embodiments. Features and processes known to those of ordinary skill may similarly be incorporated as desired. Additionally and obviously, features may be added or subtracted as desired. Accordingly, the invention is not to be restricted except in light of the attached claims and their equivalents.

CLAIMS

What is claimed is:

- 1. A universal control device for controlling a power converter, comprising: a three-phase multilevel voltage source converter coupled with a control circuit and a plurality of output capacitors, wherein the three phase multilevel voltage source converter is configured to balance the voltage on the output capacitors with one cycle control.
- 2. The control device of claim 1, wherein the power converter is one of a power factor corrected rectifier, active power filter, static synchronous compensator or a grid connected inverter.
- 3. A distributed generation power system, comprising:
 a universal one cycle control vector controller configured to control a plurality of three-phase multilevel power converters.

Document made available under the **Patent Cooperation Treaty (PCT)**

International application number: PCT/US2004/036542

01 November 2004 (01.11.2004) International filing date:

Certified copy of priority document Document type:

Country/Office: US Document details:

> 60/516,247 Number:

Filing date: 31 October 2003 (31.10.2003)

Date of receipt at the International Bureau: 23 March 2006 (23.03.2006)

Priority document submitted or transmitted to the International Bureau in Remark:

compliance with Rule 17.1(a) or (b)

